



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MEMO OF MARKS/GRADES AND CREDITS

B.Tech. **ELECTRONICS & COMMUNICATION ENGINEERING**

CMM. No. **C 1063269**

Serial No. : **21417004345**

Name : **MANDLA LIKITHA**

Hall Ticket No. **16261A04F4**

Year of Admission **2016-2017**



Name of the College : **26-M G I T, HYDERABAD**

Month & Year of Final Exam : **October, 2020**

Class Awarded : **FIRST CLASS**

S.No.	SUBJECT TITLE	GRADE POINT	GRADE	CREDITS	S.No.	SUBJECT TITLE	GRADE POINT	GRADE	CREDITS
I SEMESTER					I YEAR				
I SEMESTER					II SEMESTER				
1	ENGINEERING CHEMISTRY	5	C	4	1	MATHEMATICS - III	6	B	4
2	MATHEMATICS - I	6	B	3	2	COMPUTER PROGRAMMING IN C	5	C	3
3	ENGINEERING MECHANICS	5	C	3	3	MATHEMATICS - II	6	B	4
4	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	5	C	4	4	ENGINEERING PHYSICS - II	5	C	3
5	PROFESSIONAL COMMUNICATION IN ENGLISH	7	B+	3	5	ENGINEERING PHYSICS LAB	9	A+	2
6	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	8	A	2	6	ENGINEERING GRAPHICS	6	B	4
7	ENGINEERING PHYSICS - I	6	B	3	7	ENGINEERING CHEMISTRY LAB	9	A+	2
8	ENGINEERING WORKSHOP	9	A+	2	8	COMPUTER PROGRAMMING IN C LAB	8	A	2
I SEMESTER					II YEAR				
I SEMESTER					II SEMESTER				
1	ANALOG ELECTRONICS	5	C	4	1	PULSE AND DIGITAL CIRCUITS	5	C	4
2	SIGNALS AND STOCHASTIC PROCESS	5	C	3 *	2	BUSINESS ECONOMICS AND FINANCIAL ANALYSIS	5	C	3
3	NETWORK ANALYSIS	5	C	3	3	ANALOG COMMUNICATIONS	6	B	4
4	ELECTRICAL TECHNOLOGY	5	C	4	4	CONTROL SYSTEMS	5	C	4
5	MATHEMATICS - IV	6	B	4	5	SWITCHING THEORY AND LOGIC DESIGN	6	B	3
6	BASIC ELECTRICAL ENGINEERING LAB	9	A+	2	6	ANALOG COMMUNICATIONS LAB	8	A	2
7	ELECTRONIC DEVICES AND CIRCUITS LAB	8	A	2	7	PULSE AND DIGITAL CIRCUITS LAB	9	A+	2
8	BASIC SIMULATION LAB	9	A+	2	8	ANALOG ELECTRONICS LAB	8	A	2
9	ENVIRONMENTAL SCIENCE AND TECHNOLOGY *	-	-	0	9	GENDER SENSITIZATION LAB *	-	-	0
I SEMESTER					III YEAR				
I SEMESTER					II SEMESTER				
1	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	5	C	4	1	DIGITAL SIGNAL PROCESSING	5	C	4
2	DATABASE MANAGEMENT SYSTEMS	5	C	3 *	2	MICROPROCESSORS AND MICROCONTROLLERS	5	C	4
3	LINEAR AND DIGITAL IC APPLICATIONS	5	C	4	3	MICROPROCESSORS AND MICROCONTROLLERS LAB	7	B+	2
4	DIGITAL COMMUNICATIONS	5	C	4	4	ADVANCED ENGLISH COMMUNICATION SKILLS LAB	8	A	2
5	FUNDAMENTALS OF MANAGEMENT	5	C	3	5	DIGITAL SYSTEM DESIGN	5	C	3
6	DIGITAL COMMUNICATIONS LAB	8	A	2	6	ANTENNAS AND WAVE PROPAGATION	5	C	4
7	LINEAR IC APPLICATIONS LAB	6	B	2	7	JAVA PROGRAMMING	5	C	3
8	DIGITAL IC APPLICATIONS LAB	9	A+	2	8	DIGITAL SIGNAL PROCESSING LAB	8	A	2
9	PROFESSIONAL ETHICS *	-	-	0					
I SEMESTER					IV YEAR				
I SEMESTER					II SEMESTER				
1	INDUSTRY ORIENTED MINI PROJECT	10	O	2	1	INTRODUCTION TO INTERNET OF THINGS (MOOCS)	10	O	3
2	COMPUTER NETWORKS	5	C	3	2	AN INTRODUCTION TO ARTIFICIAL INTELLIGENCE (MOOC)	9	A+	3
3	VLSI DESIGN	5	C	4	3	RENEWABLE ENERGY SOURCES	6	B	3
4	MICROWAVE ENGINEERING	6	B	4	4	MAJOR PROJECT	10	O	15
5	INTRODUCTION TO WIRELESS AND CELLULAR COMMUNICATIONS	6	B	3					
6	VLSI AND E-CAD LAB	9	A+	2					
7	MICROWAVE ENGINEERING LAB	9	A+	2					
8	PRINCIPLES OF MODERN CDMA/ MIMO/ OFDM WIRELESS	7	B+	3					
9	SEMINAR	8	A	1					

Number of Credits registered for : **192**

Aggregate Credits Secured for best: **186**

Aggregate Marks/CGPA Secured : **6.57**

Date of Issue : **February 23, 2021**

(see overleaf for Rules concerned to award of class)

(* Mandatory course passed for award of degree)

(* Courses registered but not counted for calculation of aggregate)



S. Tanu kalyan
CONTROLLER OF EXAMINATIONS

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA.

College: 26 - M G I T, HYDERABAD

31417004346



M. Likitha



SI. No. PC 01138053

PROVISIONAL CERTIFICATE

21417004345

HT No: 16261A04F4

This is to certify that Ms. MANDLA LIKITHA

D/o MANDLA NAGARAJU

passed B. Tech. ELECTRONICS & COMMUNICATION ENGINEERING degree

examination of this University, held in October, 2020 and that she was

placed in FIRST CLASS.

She has satisfied all the requirements for the award of the degree.

Hyderabad - T.S.

S. Tanu kalyan

Controller of Examinations

[Signature]
REGISTRAR

Date: February 23, 2021

