



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003 , ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No. **K00110050**

Bachelor of Technology

Electronics & Communication Engineering

Serial No. **201009020447**

Name of the College: **PRAGATI ENGG. COLLEGE**

Name: **SYED UMARFAROOQ KHAJA MOHIDDIN**

Name & Year of Final Exam :

B.Tech 2012

Hall Ticket No. **09A35A0411**

Year of Admission **2009 - 2010**

Class Awarded: **First Class**

S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
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I YEAR

DIRECT ADMISSION INTO SECOND YEAR UNDER LATERAL ENTRY SCHEME											
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II YEAR

1 ELECTRICAL TECHNOLOGY	13	38	51	4	1 ANALOG COMMUNICATIONS	18	40	58	4
2 ELECTRONICS CIRCUIT ANALYSIS	12	28	40	4	2 CONTROL SYSTEMS	15	38	53	4
3 ENVIRONMENTAL STUDIES	14	28	42	4	3 EM WAVES & TRANSMISSION LINES	14	28	42	4
4 MATHEMATICS - III	5	6	11	0	4 OBJECT ORIENTED PROGRAMMING	13	43	56	4
5 PROBABILITY THEORY & STO. PROCESSES	9	47	56	4	5 PULSE & DIGITAL CIRCUITS	14	28	42	4
6 SIGNALS & SYSTEMS	15	35	50	4	6 SWITCHING THEORY & LOGIC DESIGN	17	28	45	4
7 ELECTRICAL TECHNOLOGY (LAB)	20	45	65	2	7 ANALOG COMMUNICATIONS (LAB)	24	47	71	2
8 ELECTRONIC CIRCUITS (LAB)	21	47	68	2	8 PULSE & DIGITAL CIRCUITS (LAB)	20	44	64	2

III YEAR

1 ANTENNAS AND WAVE PROPAGATION	15	36	51	4	1 DIGITAL SIGNAL PROCESSING	16	28	44	4
2 COMPUTER ORGANIZATION	18	47	65	4	2 MANAGEMENT SCIENCE	19	34	53	4
3 DIGITAL COMMUNICATIONS	17	31	48	4	3 MICRO PROCESSORS AND INTERFACING	16	36	52	4
4 DIGITAL IC APPLICATIONS	17	31	48	4	4 MICROWAVE ENGINEERING	12	36	48	4
5 LINEAR IC APPLICATIONS	17	41	58	4	5 TELECOMMUNICATION SWITCHING SYSTEM	19	48	67	4
6 MANAGERIAL ECONOMICS AND FINANCIAL	14	49	63	4	6 VLSI DESIGN	15	53	68	4
7 IC APPLICATIONS AND ECAD LAB.	22	36	58	2	7 ADVANCED ENGLISH COMMUNICATION SK.	21	42	63	2
8 DIGITAL COMMUNICATIONS LAB.	22	47	69	2	8 MICRO PROCESSORS AND INTERFACING LAB	23	34	57	2

IV YEAR

1 CELLULAR AND MOBILE COMMUNICATIONS	16	47	63	4	1 COMPREHENSIVE VIVA	0	89	89	2
2 COMPUTER NETWORKS	19	31	50	4	2 DIGITAL DESIGN THROUGH VERILOG	12	40	52	4
3 DIGITAL IMAGE PROCESSING	13	35	48	4	3 OPTICAL COMMUNICATIONS	19	53	72	4
4 ELECTRONIC MEASUREMENTS & INSTRUMENTATION	16	34	50	4	4 WIRELESS COMMUNICATIONS AND NETWORKS	20	36	56	4
5 OPERATING SYSTEMS	15	44	59	4	5 SEMINAR	46	—	46	2
6 RADAR SYSTEMS	18	58	76	4	6 INDUSTRY ORIENTED MINI PROJECT	—	47	47	2
7 DIGITAL SIGNAL PROCESSING LAB	22	41	63	2	7 PROJECT WORK	36	150	186	10
8 MICROWAVE AND OPTICAL COMMUNICATIONS LAB	21	42	63	2					

Number of Credits registered for: **168**

Aggregate Marks Secured for best: **160 Credits 2695 out of 4250 (63.41 %)**

Date of Declaration of Result: **May 2012**

28/01/2013

(See overleaf for Instructions)

(*Courses registered but not countered for calculation of aggregate)

CONTROLLER OF EXAMINATIONS

Chinn