



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD, ANDHRA PRADESH, INDIA
CONSOLIDATED MARKS MEMO / CREDIT SHEET



Roll No.: **C 390584** **BACHELOR OF TECHNOLOGY – ELECTRICAL & ELECTRONICS ENGINEERING**
 Date of Birth: **492587687274** Name of the College: **K. J. Somaiya Institute of Engineering & Information Technology**
 Name: **LAKSHMI NARAYANA K** Month & Year of Final Exam: **May, 2006**
 Roll Ticket No.: **02F41A0221** Year of Admission: **2002-2003** Class Awarded: ***** SECOND CLASS *****

COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S. No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL
THEORY	20	80	100			LAB	25	50	75
I YEAR									
ENGLISH	16	36	52	6	2	MATHEMATICS - I	12	28	40
SOLID STATE PHYSICS	12	32	44	4	4	INFORMATION TECH. & NUME. METHODS	14	29	43
C & DATA STRUCTURES	15	28	43	6	6	NETWORK THEORY	14	48	62
ELECTRONICS DEVICES & CIRCUITS	8	35	43	6	8	ENGINEERING DRAWING PRACTICE	13	26	39
COMPUTER PROGRAMMING (LAB)	18	22	40	8	10	ELECTRONICS DEVICES & CKTS (LAB)	17	18	35
II SEMESTER									
MATHEMATICS - II	11	35	46	4	1	MANAGERIAL ECONOMICS & FINANCIAL A	7	33	40
HYDRAULICS & HYDRAULIC MACHINERY	6	35	41	4	2	MATHEMATICS - III	6	50	56
PULSE & DIGITAL CIRCUITS	13	29	42	4	3	LINEAR & DIGITAL IC APPLICATIONS	10	45	55
SWITCHING THEORY & LOGIC DESIGN	14	28	42	4	4	POWER SYSTEMS - I	2	38	40
ELECTROMAGNETIC FIELDS	12	29	41	4	5	ELECTROMECHANICS - II	6	34	40
ELECTRO MECHANICS - I	11	29	40	4	6	CONTROL SYSTEMS	4	46	50
HYDRAULICS & HYDRAULIC MACHINERY (	15	30	45	2	7	IC & PULSE & DIGITAL CIRCUITS (LAB)	3	32	35
NETWORKS (LAB)	23	42	65	2	8	ELECTROMECHANICS (LAB) - I	18	18	36
III SEMESTER									
COMPUTER ORGANIZATION	12	28	40	4	1	ANALYSIS OF LINEAR SYSTEMS	11	40	51
ELECTRICAL MEASUREMENTS	14	33	47	4	2	MICROPROCESSORS AND INTERFACING	9	31	40
POWER SYSTEMS-II	10	34	44	4	3	MODELLING OF POWER SYSTEM COMPONENTS	7	33	40
POWER ELECTRONICS	10	30	40	4	4	INSTRUMENTATION	11	35	46
ELECTROMECHANICS-III	10	30	40	4	5	POWER SYSTEMS - III	6	34	40
MANAGEMENT SCIENCE	10	37	47	4	6	UTILIZATION OF ELECTRICAL ENERGY	12	37	49
ELECTROMECHANICS (LAB) - II	19	31	50	2	7	ELECTRICAL MEASUREMENTS LAB	15	30	45
CONTROL SYSTEMS (LAB)	13	21	34	2	8	POWER ELECTRONICS LAB	12	26	38
IV SEMESTER									
DIGITAL SIGNAL PROCESSING	18	33	51	4	1	H V D C TRANSMISSION	11	33	44
POWER SEMICONDUCTOR DRIVES	12	28	40	4	2	DATABASE MANAGEMENT SYSTEMS	13	28	41
COMPUTER METHODS IN POWER SYS.	9	36	45	4	3	PROJECT & SEMINAR #	32	145	177
POWER SYSTEM OPERATION & CONTROL	8	38	46	4					
H V ENGINEERING	15	30	45	4					
NEURAL NETWORKS & APPLICATIONS	15	28	43	4					
MICROPROCESSORS LAB	20	26	46	2					
SIMULATION OF ELEC. SYS. LAB	18	20	38	2					

Number of Credits registered for : 212 (# Project Internal = 40, External = 160)
 Aggregate Marks Secured for best : 212 CREDITS TOTAL 3002 OUT OF 5075 (58.51 %)

Date of Declaration of Result : June 6, 2006
 (See overleaf for instructions) (*Courses registered but not counted for calculation of aggregate)

B. Sanyal Kumar
 CONTROLLER OF EXAMINATIONS