



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET

S.Tech. ELECTRICAL & ELECTRONICS ENGINEERING

Exam. No. **c 0359173**

Ser. No.: 21277021881

Name: **BANDARI SAHITHI**

Hall Ticket No.: **14JJ1A0202**

Year of Admission: 2014-2015

Name of the College: JJ-JNTUHCE JAGTIAL

Month & Year of Final Exam: April 2018

Class Awarded: **FIRST CLASS WITH DISTINCTION**



S.No.	SUBJECT TITLE	INT. MARKS	SE. MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT. MARKS	SE. MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	75	100	
I YEAR											
1	ENGLISH-I	22	54	76	4	2	MATHEMATICS - I	22	56	78	6
3	MATHEMATICAL METHODS	24	53	77	6	4	ENGINEERING PHYSICS	21	54	75	6
5	ENGINEERING CHEMISTRY	18	65	83	6	5	COMPUTER PROGRAMMING	21	44	65	6
7	ENGINEERING DRAWING	21	37	58	6	6	COMPUTER PROGRAMMING LAB	22	43	65	4
9	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY - A	25	45	70	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	45	68	4
11	ENGINEERING WORKSHOP / IT WORKSHOP	21	45	66	4						
I SEMESTER											
II YEAR											
1	MATHEMATICS - II	20	61	81	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	23	41	64	4
2	FLUID MECHANICS AND HYDRAULIC MACHINERY	20	59	79	4	2	POWER SYSTEMS - I	18	54	72	4
3	ELECTRONIC DEVICES & CIRCUITS	24	50	74	4	3	ELECTRONIC CIRCUITS	21	55	76	4
4	ELECTRICAL CIRCUITS	22	37	59	4	4	SWITCHING THEORY AND LOGIC DESIGN	23	44	67	4
5	ELECTROMAGNETIC FIELDS	25	26	51	4	5	NETWORK THEORY	21	42	63	4
6	ELECTRICAL MACHINES - I	22	39	61	4	6	ELECTRICAL MACHINES - I	20	38	58	4
7	FLUID MECHANICS AND HYDRAULIC MACHINERY LAB	22	49	71	2	7	ELECTRICAL MACHINES LAB - I	20	38	58	2
8	ELECTRONIC DEVICES AND CIRCUITS LAB	20	38	58	2	8	ELECTRICAL CIRCUITS AND SIMULATION LAB	22	44	66	2
						9	GENDER SENSITIZATION	23	48	71	2
I SEMESTER											
III YEAR											
1	IC APPLICATIONS	24	42	66	4	1	ELECTRICAL AND ELECTRONICS INSTRUMENTATION	24	47	71	4
2	MANAGEMENT SCIENCE	22	30	52	4	2	STATIC DRIVES	23	56	79	4
3	POWER SYSTEMS - I	21	53	74	4	3	COMPUTER METHODS IN POWER SYSTEMS	22	57	79	4
4	CONTROL SYSTEMS	22	46	68	4	4	MICROPROCESSORS AND INTERFACING DEVICES	24	38	62	4
5	POWER ELECTRONICS	18	39	55	4	5	ENVIRONMENTAL STUDIES	23	37	60	4
6	ELECTRICAL MACHINES - II	21	50	71	4	6	DISASTER MANAGEMENT	23	52	75	4
7	ELECTRICAL MACHINES LAB - I	23	45	68	2	7	CONTROL SYSTEMS AND SIMULATION LAB	24	48	72	2
8	ADVANCED COMMUNICATION SKILLS LAB	21	44	65	2	8	POWER ELECTRONICS AND SIMULATION LAB	24	48	72	2
I SEMESTER											
IV YEAR											
1	SWITCHGEAR AND PROTECTION	24	45	69	4	1	FUNDAMENTALS OF HVDC AND FACTS DEVICES	24	34	58	4
2	UTILIZATION OF ELECTRICAL ENERGY	24	48	72	4	2	RENEWABLE ENERGY SOURCES	21	39	60	4
3	DIGITAL SIGNAL PROCESSING	21	36	57	4	3	EHV AC TRANSMISSION	23	43	66	4
4	POWER SYSTEM OPERATION AND CONTROL	24	41	65	4	4	INDUSTRY ORIENTED MINI PROJECT	-	45	45	2
5	HIGH VOLTAGE ENGINEERING	24	34	58	4	5	SEMINAR	45	-	45	2
6	ELECTRICAL DISTRIBUTION SYSTEMS	23	57	80	4	6	PROJECT WORK	40	137	177	10
7	MICROPROCESSORS AND INTERFACING DEVICES LAB	23	47	70	2	7	COMPREHENSIVE Viva-voce	-	90	90	2
8	ELECTRICAL MEASUREMENTS LAB	24	48	72	2						

(A) Project: Internal=50, External=150

Number of Credits registered for: 226 Aggregate Marks Secured for best: 218

Aggregate Marks Secured: **3905 OUT OF 5250 (74.38%)**

Date of Issue: 29 May, 2018

Free overleaf for Rules concerned to award of degree

(A) Credits considered for award of degree

(B) Courses registered but not counted for consideration of aggregate



S. Tanu kalyan

CONTROLLER OF EXAMINATIONS

A. in cases: ABSENT