



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2009
 Electronics & Communication Engineering



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08 098070

Name of the Student : CHETHAN KALAPPAN S

Father's / Mother's Name : N K SOMANNA

Seat No. : 4KV07EC029

Name of the College : KURUNJI VENKATRAMANA GOWDA COLLEGE OF ENGINEERING, SULLIA(D.K.)

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06MAT31	Engineering Mathematics -III	100	35	55	25	-	23	125	50	78	Pass
2	06ES32	Analog Electronic Circuits	100	35	47	25	-	19	125	50	66	Pass
3	06ES33	Logic Design	100	35	62	25	-	20	125	50	82	Pass
4	06ES34	Network Analysis	100	35	81	25	-	18	125	50	99	Pass
5	06IT35	Electronic Instrumentation	100	35	35	25	-	20	125	50	55	Pass
6	06ES36	Field Theory	100	35	35	25	-	19	125	50	54	Pass
7	06ESL37	Analog Electronics Lab	50	20	45	25	12	20	75	32	65	Pass
8	06ESL38	Logic Design Lab	50	20	28	25	12	24	75	32	52	Pass
GRAND TOTAL									900	364	551	

Total Marks Obtained (in words) : FIVE HUNDRED AND FIFTY ONE ONLY

Result of the Semester : FIRST CLASS

DATE : MAY 19, 2009

A - Absent NE - Not Eligible



Registrar (Evaluation)