

VIDYALANKAR INSTITUTE OF TECHNOLOGY

Vidyalankar Marg, Wadala (E), Mumbai 400 037
(Affiliated to University of Mumbai)

GRADE CARD

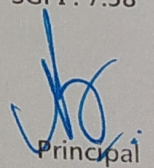
Name : KHARAT VAIBHAV NAVINDRANATH TARABAI Seat No. : 336092
Examination : Second Year ELECTRONICS ENGINEERING (Semester 3)
of syllabus Revised (2016) held in November-2017

Scheme Code	Course Title	Head/s of Passing	Course Credits	Grade Obtained	Credits Earned (C)	Grade Point (G)	C x G
ELX301	APPLIED MATHEMATICS III	ESE+IA	04	D	04	06	24
		TW	01	O	01	10	10
ELX302	ELECTRONIC DEVICES AND CIRCUITS I	ESE+IA	04	C	04	07	28
ELX303	DIGITAL CIRCUIT DESIGN	ESE+IA	04	A	04	09	36
ELX304	ELECTRICAL NETWORK ANALYSIS AND SYNTHESIS	ESE+IA	04	D	04	06	24
ELX305	ELECTRONIC INSTRUMENTS AND MEASUREMENTS	ESE+IA	04	C	04	07	28
ELXL301	ELECTRONIC DEVICES AND CIRCUITS I LABORATORY	ORAL+TW	01	A	01	09	09
ELXL302	DIGITAL CIRCUIT DESIGN LABORATORY	ORAL+TW	01	O	01	10	10
ELXL303	ELECTRICAL NETWORK AND MEASUREMENT LABORATORY	TW	01	O	01	10	10
ELXL304	OBJECT ORIENTED PROGRAMMING METHODOLOGY LABORATORY	ORAL+TW	02	A	02	09	18
	Total		26	-	26	83	197

Result : PASSES

SGPI : 7.58

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Principal

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